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ORIGINALLY FILED

Jc. Docket No.: RPS920010132US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date:

February 14, 2002

In re Application of:

R. T. Bailis et al.

Serial Number:

10/015,921

Filed:

December 10, 2001

For:

Method and System for Use of a Field Programmable Gate

Array (FPGA) Cell for Controlling Access to On-Chip

Functions of a System on a Chip (SOC) Integrated Circuit

Chip

Win

Art Unit:

2811

Examiner:

CERTIFICATE OF MAILING UNDER 37 CFR 1.8(a)

Assistant Commissioner for Patents Washington, D.C. 20231

I hereby certify that the following attached correspondence comprising:

- 1. Transmittal of Corrected Application Papers
- 2. Notice to File Corrected Application Papers
- 3. Formals Drawings (2 sheets)
- 4. Reply post card

is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

> **BOX MISSING PARTS Assistant Commissioner for Patents** Washington, D.C. 20231

on	February	14,	2002
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Karen Orzechowski		
(Name of person mailing paper or fee)		







COMMISSIONER FOR PATENTS
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Washington, D.C. 20231
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APPLICATION NUMBER

FILING/RECEIPT DATE

FIRST NAMED APPLICANT

ATTORNEY DOCKET NUMBER

10/015,921

41.1

Shirth Street

12/10/2001

Robert Thomas Bailis

RPS920010132US1

CONFIRMATION NO. 5851FORMALITIES LETTER

OC00000007300169

Joscelyn G. Cockburn IBM Corpaoaration 9CCA/B002 PO Box 12195 Research Triangle Park, NC 27709

Date Mailed: 01/11/2002

NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

A copy of this notice MUST be returned with the reply.

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Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE